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Mani Adeli

A
U.S. PTO
12/19/00
**UTILITY
PATENT APPLICATION
TRANSMITTAL**

Attorney Docket No. SPLX.P0017

First Named Inventor: Steven Teig

Title: Method and Apparatus for Quantifying the Quality of Placement Configurations in a Partitioned Region of an Integrated-Circuit Layout

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APPLICATION ELEMENTS

See MPEP chapter 600 concerning utility patent application contents.

ADDRESS TO: Assistant Commissioner for Patents
Box Patent Application
Washington, DC 20231

Transmitted herewith for filing under the continuing application procedure of 37 CFR §1.53(b) is a patent application as identified above. Included with this transmittal are the following:

1. Fee Transmittal Form
2. Applicant claims small entity status.
3. Specification (101 Total Pages)
4. Drawing(s) (35 U.S.C. 113) (39 Total Sheets)
5. Declaration and Power of Attorney (Total Pages)
 - a. Newly executed (original or copy)
 - b. Copy from a prior application (37 CFR §1.63(d))
6. Assignment Papers (cover sheet & document(s))
7. Preliminary Amendment
8. Return Receipt Postcard
9. Request and Certification under 35 U.S.C. 122(b)(2)(B)(i).
10. **This application is a CONTINUATION of:**

Application Title: RECURSIVE PARTITIONING PLACEMENT METHOD AND APPARATUSFiling date: 12/6/00 Express Mail Label No.: EL714233199US

For CONTINUATION or DIVISIONAL APPS: The entire disclosure of the prior application is considered a part of the disclosure of the accompanying continuation or divisional application and is hereby incorporated by reference. The incorporation can only be relied upon when a portion has been inadvertently omitted from the submitted application parts.

11. Please cancel and/or add claims according to the accompanying preliminary amendment before calculating the application filing fee.

12. CORRESPONDENCE ADDRESS:

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Name	Mani Adeli	Registration Number:	39,585
Signature	<i>Mani Adeli</i>	Date:	December 19, 2000

REQUEST AND CERTIFICATION UNDER 35 U.S.C. 122(b)(2)(B)(i)	First Named Inventor	Steven Teig
	Title	Method and Apparatus for Quantifying the Quality of Placement Configurations in a Partitioned Region of an Integrated-Circuit Layout
	Atty Docket Number	SPLX.P001

00017421121920

I hereby certify that the invention disclosed in the attached application has not and will not be the subject of an application filed in another country, or under a multilateral agreement, that requires publication at eighteen months after filing. I hereby request that the attached application not be published under 35 U.S.C. 122(b).

December 19, 2000

Date

Mani Adeli
Signature

Mani Adeli
Typed or printed name

This request must be signed in compliance with 37 CFR 1.33(b) and submitted with the application upon filing.

Applicant may rescind this nonpublication request at any time. If applicant rescinds a request that an application not be published under 35 U.S.C. 122(b), the application will be scheduled for publication at eighteen months from the earliest claimed filing date for which a benefit is claimed.

If applicant subsequently files an application directed to the invention disclosed in the attached application in another country, or under a multilateral international agreement, that requires publication of applications eighteen months after filing, the applicant must notify the United States Patent and Trademark Office of such filing within forty-five (45) days after the date of the filing of such foreign or international application. **Failure to do so will result in abandonment of this application (35 U.S.C. 122(b)(2)(B)(iii)).**

Burden Hour Statement: This collection of information is required by 37 CFR 1.213(a). The information is used by the public to request that an application not be published under 35 U.S.C. 122(b) (and the PTO to process that request). Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This form is estimated to take 6 minutes to complete. This time will vary depending upon the needs of the individual case. Any comments on the amount of time you are required to complete this form should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, Washington, DC 20231. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Assistant Commissioner for Patents, Washington, DC 20231.